

1097 U.S. PTO  
09/625418

Subclass	Class
4	703/01
ISSUE CLASSIFICATION	

PATENT NUMBER

Jac  
866

U.S. UTILITY Patent Application

MH O.I.P.E. PATENT DATE  
SCANNED 1/12 Q.A. 1/12

APPLICATION NO. 09/825418	CONT/PRIOR F	CLASS 174	SUBCLASS 256	ART UNIT 2851	EXAMINER <i>Dinh</i>
APPLICANTS Yoshio Arai Shinya Watanabe Satoshi Shiratori Hirosaki Ono					
TITLE Niricon board comprising a printed circuit board having a plurality of conductive patterns and a resinous insulating layer covering the conductive patterns.					
PTO-2040 12/99					

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
[Continued on Issue Slip Inside File Jacket]					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368.  
Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-488A  
(Rev. 8/99)

FILED WITH:  DISK (CRF)  FICHE  CD-ROM

(Attached in pocket on right inside flap)

(FACE)